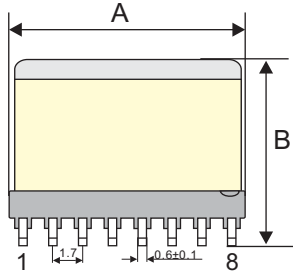
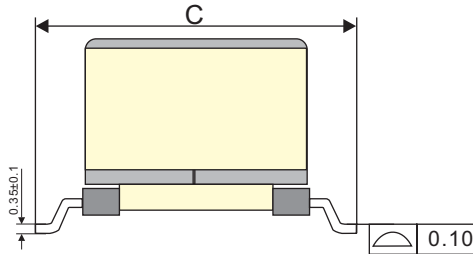


### 1. Mechanical Dimensions:

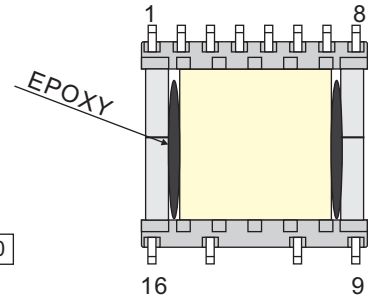
**FRONT VIEW**



**SIDE VIEW**



**BOTTOM VIEW**

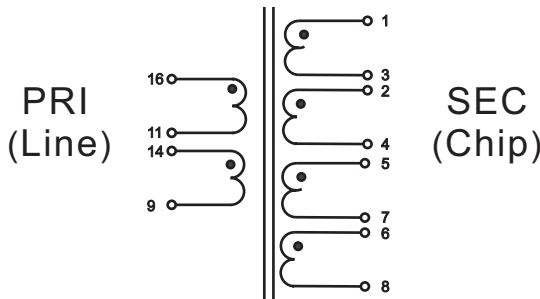


Note:

- 1.Wrap 2Ts of 7mm tape around the finished part.

### 2.SCHEMATIC:

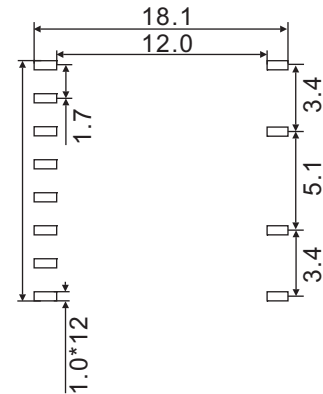
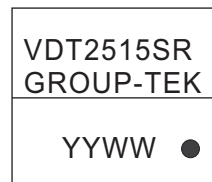
Suggested PCB Paad Layout



Unit: mm

A MAX	B MAX	C MAX
14.0	13.0	17.3

Marking information



### 3.Electrical Characteristics :

Specifications: at 25 degree C. Operating Temperature -40 to +85°C

NO.	ITEM	TEST CONDITION	SPECIFICATION
1	Inductance	(16-9) tied(11+14)@10kHz, 0.1v	400uH+/-10%
2	DCR	(5-8) TIED(6+7) @ 25°C	0.50Ω Max
3		(1-4) TIED(1+2,3+4) @ 25°C	0.42Ω Max
4		(16-9) TIED(11+14) @ 25°C	1.55Ω Max
5	HI-POT	PRI TO SEC	1875Vdc 1mA 1Sec.
6	Leakage Inductance	(16-9) @100kHz,0.1Vac tied (14+11,1+2+3+4,5+6+7+8)	2.0uH Max.
7	T/R	(16-9):(1-4) TIED(11+14,1+2,3+4)	2.7:1±2%
8		(16-9):(5-8) TIED(11+14,6+7)	2.7 :1±2%
9	THD	@30kHz,3.0Vac(16-9):(1-4) TIED(11+14,1+2,3+4) Load 5.6Ω	-82dB Max.